





RJ45 MAGNETIC JACK WITHOUT LED, 8P, 8C SHIELDED/10/100 Mbps FILTER

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> ACAD REFERENCE NO .: DRAWN: R.STA.MONICA PART NUMBER CHECKED: A63-113-300P422 DATE: DATE: NOV.19/2010

DRAWING NUMBER A63-112-300P422

SEE NOTE

SHEET 1 OF 2

ISSUE

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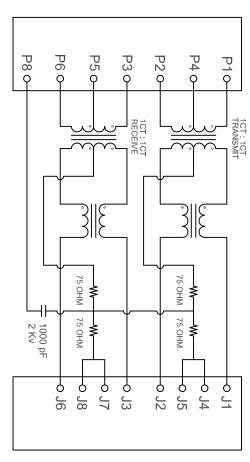
ELECTRICAL CIRCUIT:

0.0 TOLERANCE UNLESS OTHERWISE SPECIFIED IN MM: ± 0.38 ± 0.25 ± 0.10 ANGLE

DO NOT MAKE MANUAL REVISIONS TO MASTER



CONNECTOR SOLDER SIDE RJ45 CONTACT SIDE



1.TR:(100KHz,0.1V), TEST NOTES:(25±5°C) PINS:(P1-P2):(J1-J2)=1:1±3%

2.LX:(100KHz,100mV,8mA, DC Bias) PINS:(P3-P6):(J3-J6)=1:1±3%

PINS: (P1,P2),(P3,P6)=350uH MINIMUM

3.DC RESISTANCE:

PINS:(J1-J2),(J3-J6)= 1.2 OHMS MAXIMUM

PINS(P3,P6)TO(J3,J6)=1500VAC OR 2250VDC PINS(P1,P2)TO(J1,J2)=1500VAC OR 2250VDC

5.INSERTION LOSS:

-1.2dB MAXIMUM AT 100KHz TO 100MHz

6 RETURN LOSS

- -18dB MINIMUM AT 1MHz TO 30MHz;
- -(18-20log[f/30])dB MINIMUM AT 30MHz TO 60MHz
- -12dB MINIMUM AT 60MHz TO 80MHz
- 7 CROSS TALK
- -35dB TYPICAL AT 1MHz TO 100MHz
- 8.COMMON TO COMMON MODE ATENUATION: -30dB TYPICAL AT 30MHz TO 100MHz
- 9. LEAKAGE INDUCTANCE:
- PINS: P3-P6, WITH J3-J6 SHORTED=0.35 uH MAX @1MHz PINS: P1-P2, WITH J1-J2 SHORTED=0.35 uH MAX @1MHz
- 10. INTERWINDING CAPACITANCE: PINS:(P3-P6):(J3-J6)= 35 pF MAX @1MHz PINS:(P1-P2):(J1-J2)= 35 pF MAX @1MHz

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EUROPEAN UNION DIRECTIVES 2002/95/EC THIS SERIES FULLY CONFORMS TO THE AND 2002/96/EC FOR RoHS COMPLIANCY.

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