



# CPC1983YE Normally Open Power SIP Relay

Parameter	Rating	Units
Blocking Voltage	600	V <sub>P</sub>
Load Current	0.5	A <sub>rms</sub> / A <sub>DC</sub>
On-Resistance (max)	6	Ω
Input / Output Isolation	4	kV

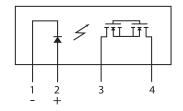
## **Features**

- 4000V<sub>rms</sub> Input/Output Isolation
- Handle Load Currents Up to 0.5A
- 600V<sub>P</sub> Blocking Voltage
- Power SIP Package
- · High Reliability
- · No Moving Parts
- Low Drive Power Requirements (TTL/CMOS Compatible)
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable

# **Applications**

- Industrial Controls
- Motor Control
- Robotics
- Medical Equipment—Patient/Equipment Isolation
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - · Electronic Switching
  - I/O Subsystems
- · Meters (Watt-Hour, Water, Gas)
- IC Equipment
- Home Appliances

# **Pin Configuration**



## **Description**

IXYS Integrated Circuits Division and its parent, IXYS, have combined to bring OptoMOS® technology, reliability, and compact size to a new family of high-power Solid State Relays.

As part of this family, the CPC1983YE single-pole normally open (1-Form-A) Solid State Power Relay is rated for up to  $0.5A_{DC}$  /  $0.5A_{rms}$  continuous load current.

The CPC1983YE employs optically coupled MOSFET technology to provide 4000V<sub>rms</sub> of input to output isolation. The optically coupled outputs, that use patented OptoMOS architecture, are controlled by a highly efficient GaAlAs infrared LED.

This combination of low on-resistance and high load current handling capability makes this relay suitable for a variety of high performance switching applications.

The "E" suffix signifies an enhanced isolation voltage.

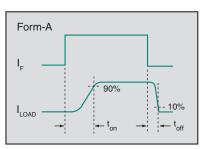
## **Approvals**

UL Recognized Component: File E69938

# Ordering Information

Part #	Description
CPC1983YE	Power SIP Package (25 per tube)

## Switching Characteristics of Normally Open Devices











# Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	600	V <sub>P</sub>
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	А
Input Power Dissipation <sup>1</sup>	150	mW
Total Power Dissipation <sup>2</sup>	1600	mW
Isolation Voltage, Input to Output 3	4000	$V_{\rm rms}$
ESD Rating, Human Body Model	8	kV
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

## Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics						
Load Current, Continuous	I <sub>F</sub> =5mA, Free air	IL	-	-	0.5	$A_{rms}/A_{DC}$
Peak Load Current	I <sub>F</sub> =5mA, t=10ms	I <sub>LPK</sub>	-	-	±3	Ap
On-Resistance <sup>1</sup>	$I_F=5mA$ , $I_L=0.5A$	R <sub>ON</sub>	-	3.5	6	Ω
Off-State Leakage Current	$I_F=0mA, V_L=600V_P$	I <sub>LEAK</sub>	-	-	1	μΑ
Switching Speeds						
Turn-On	I 5mA V 10V	t <sub>on</sub>	-	2.2	5	m.o.
Turn-Off	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>off</sub>	-	0.15	2	ms
Output Capacitance	I <sub>F</sub> =0mA, 50V, f=1MHz	C <sub>OUT</sub>	-	41	-	pF
Input Characteristics			1		1	-
Input Control Current to Activate	I <sub>1</sub> =0.5A	I <sub>F</sub>	-	1.3	5	mA
Input Control Current to Deactivate	-	I <sub>F</sub>	0.5	-	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μΑ
Input/Output Characteristics		2.		•		
Capacitance Input/Output	f=1MHz	C <sub>I/O</sub>	-	2	-	pF

<sup>&</sup>lt;sup>1</sup> Measurement taken within 1 second of on-time.

## **Thermal Characteristics**

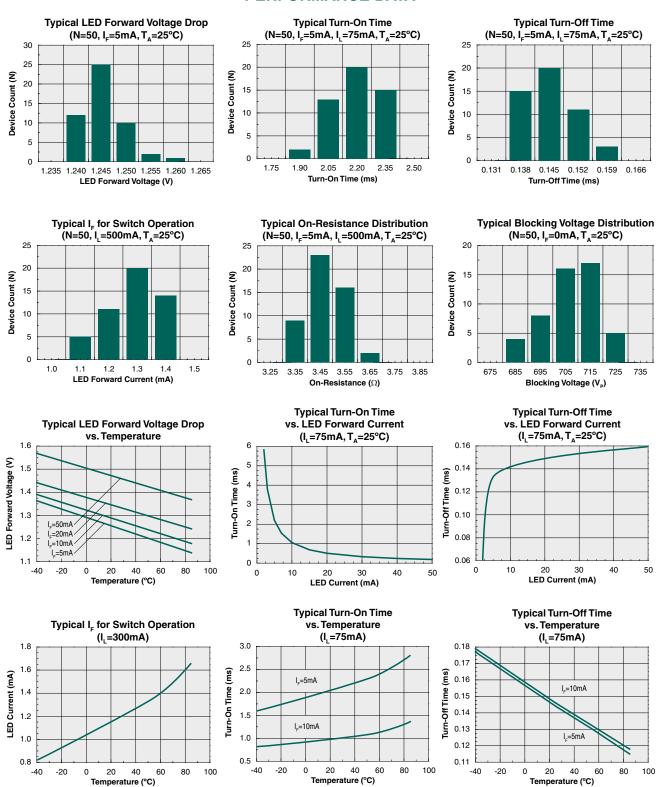
Parameter	Conditions	Symbol	Min	Тур	Max	Units
Thermal Resistance (junction to case)	-	$R_{\theta JC}$	-	1.5	-	°C/W

Derate linearly 3.33 mW / °C
Derate linearly 16.667 mW / °C

 $<sup>^3\,</sup>$  UL File 69938 is only rated by UL to 2500V  $_{\rm rms}.$ 



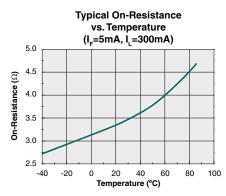
#### **PERFORMANCE DATA\***

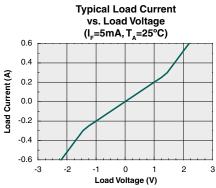


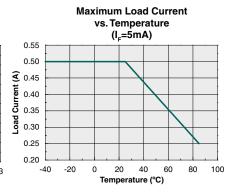
<sup>\*</sup>The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

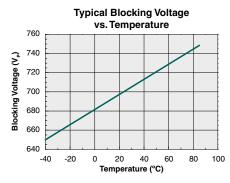


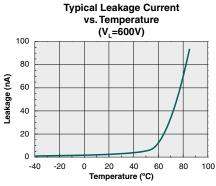
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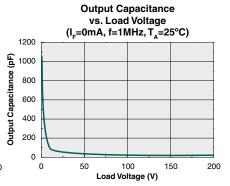


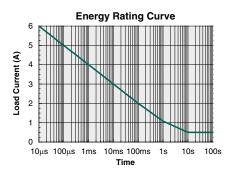












<sup>\*</sup>The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



## **Manufacturing Information**

## **Moisture Sensitivity**

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
CPC1983YE	MSL 1

#### **ESD Sensitivity**



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

## **Reflow Profile**

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time		
CPC1983YE	245°C for 30 seconds		

#### **Board Wash**

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.



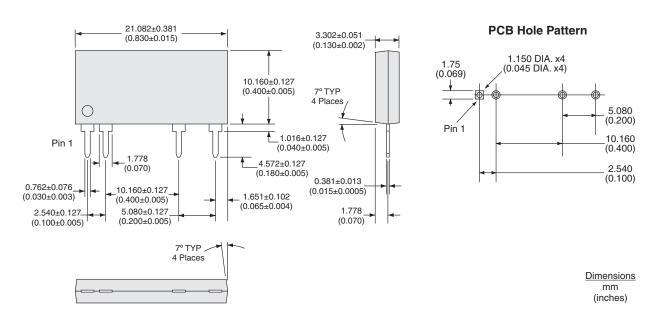






#### **MECHANICAL DIMENSIONS**

## **CPC1983YE**



#### For additional information please visit our website at: www.ixysic.com

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