

DUAL PNP SMALL SIGNAL SURFACE MOUNT TRANSISTOR
Features

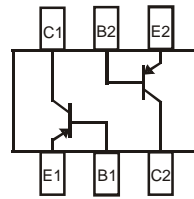
- Epitaxial Planar Die Construction
- Small Surface Mount Package
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**

Mechanical Data

- Case: SOT-26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.016 grams (approximate)



Top View



Device Schematic

Maximum Ratings @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CB0}	-60	V
Collector-Emitter Voltage	V_{CEO}	-50	V
Emitter-Base Voltage	V_{EBO}	-5.0	V
Continuous Collector Current	I_C	-500	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ $T_A = 25^\circ\text{C}$	P_D	300	mW
Thermal Resistance, Junction to Ambient Air (Note 3) @ $T_A = 25^\circ\text{C}$	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)						
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	-60	—	—	V	$I_C = -100\mu\text{A}$
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	-50	—	—	V	$I_C = -1.0\text{mA}$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-5.0	—	—	V	$I_E = -100\mu\text{A}$
Collector Cutoff Current	I_{CBO}	—	—	-0.1	μA	$V_{CB} = -30\text{V}$
Emitter Cutoff Current	I_{EBO}	—	—	-0.1	μA	$V_{EB} = -4.0\text{V}$
ON CHARACTERISTICS (Note 4)						
DC Current Gain	h_{FE}	120	—	390	—	$V_{CE} = -3.0\text{V}, I_C = -100\text{mA}$
Collector-Emitter Saturation Voltage (Note 3)	$V_{CE(SAT)}$	—	—	-0.6	V	$I_C = -500\text{mA}, I_B = -50\text{mA}$
SMALL SIGNAL CHARACTERISTICS						
Gain Bandwidth Product	f_T	—	200	—	MHz	$V_{CE} = -5\text{V}, I_E = 20\text{mA}, f = 100\text{MHz}$
Output Capacitance	C_{ob}	—	7	—	pF	$V_{CB} = -10\text{V}, I_E = 0, f = 1\text{MHz}$

- Notes:
1. No purposefully added lead.
 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 3. Device mounted on FR-4 PCB; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on page 4 or on our website at <http://www.diodes.com/datasheets/ap02001.pdf>.
 4. Short duration pulse test used to minimize self-heating effect.

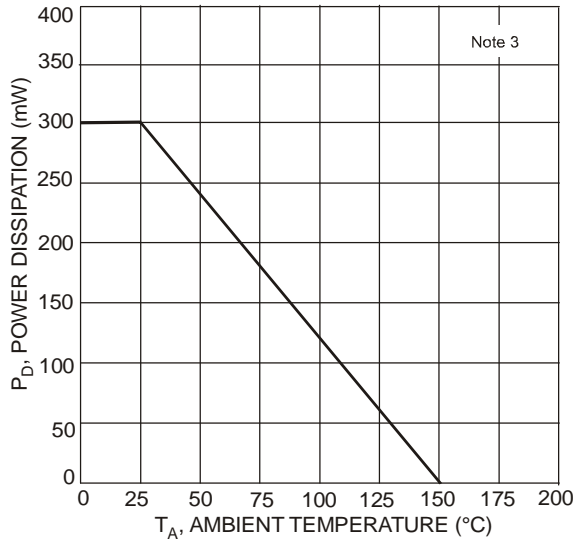


Fig. 1, Max Power Dissipation vs. Ambient Temperature

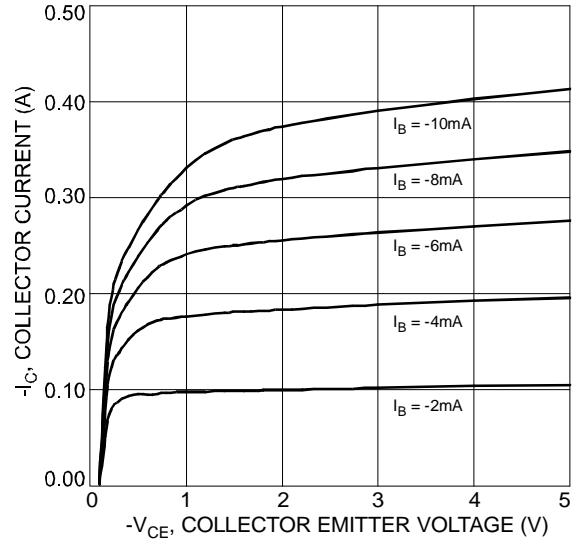


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

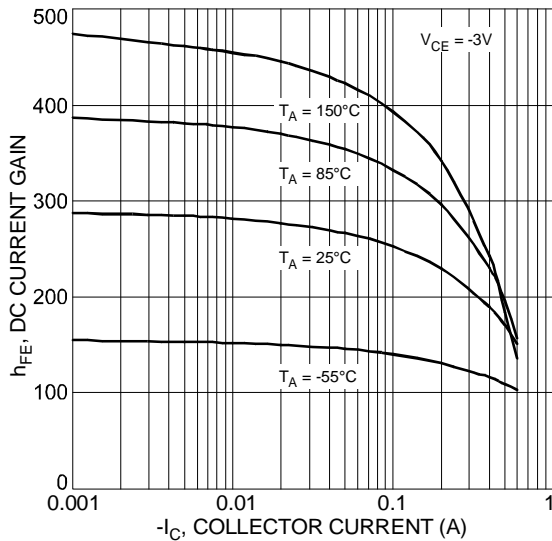


Fig. 3 Typical DC Current Gain vs. Collector Current

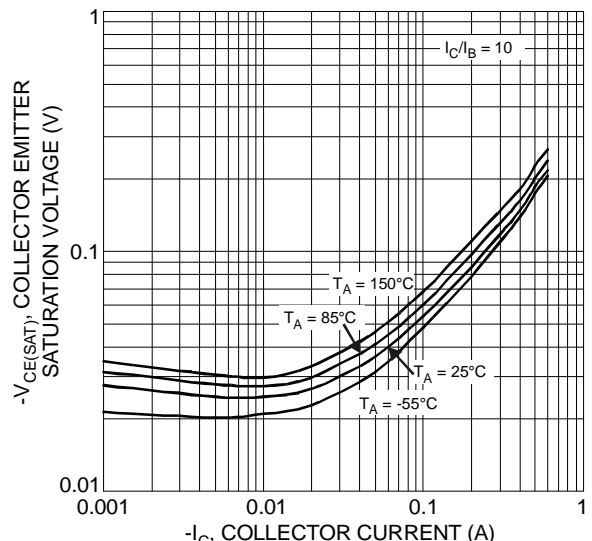


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

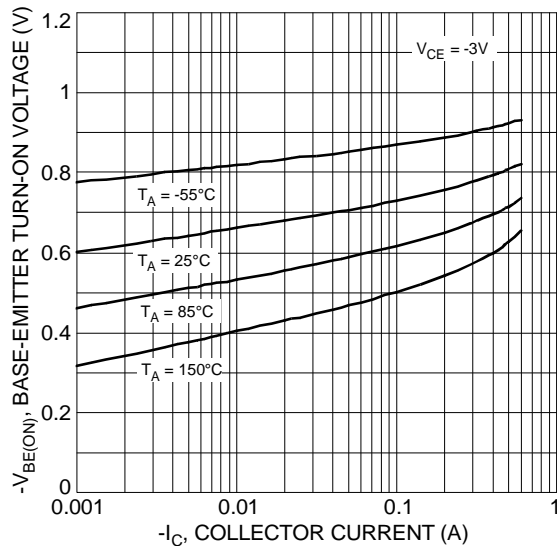


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

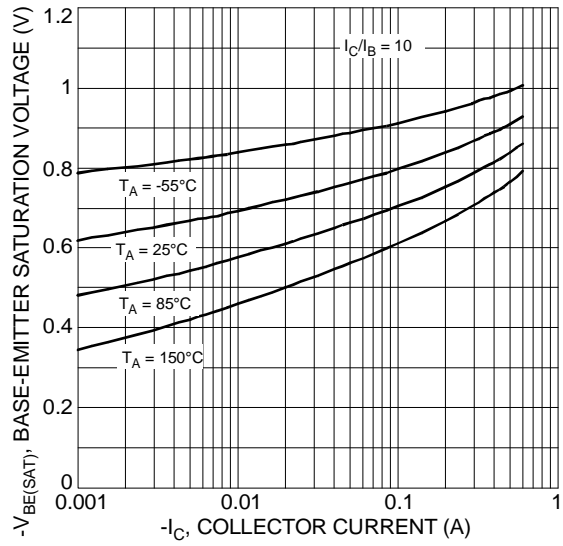


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

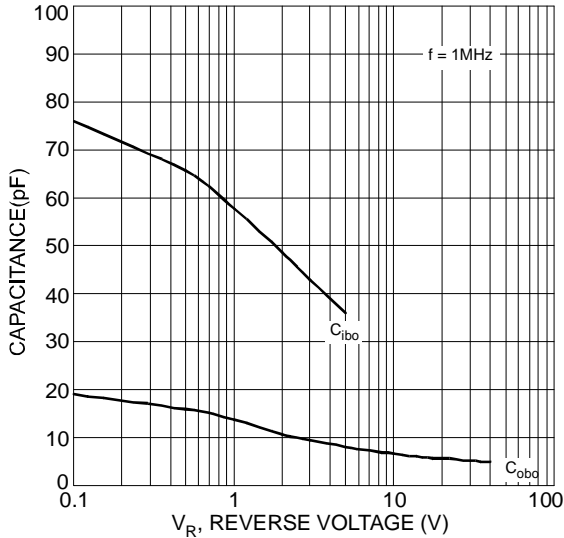


Fig. 7 Typical Capacitance Characteristics

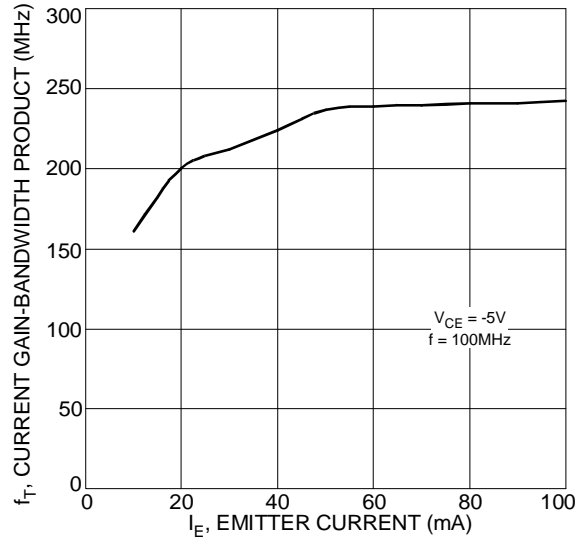


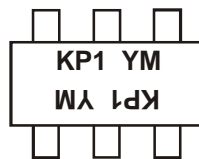
Fig. 8 Typical Gain-Bandwidth Product vs. Emitter Current

Ordering Information (Note 5)

Part Number	Case	Packaging
IMT17-7	SOT-26	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



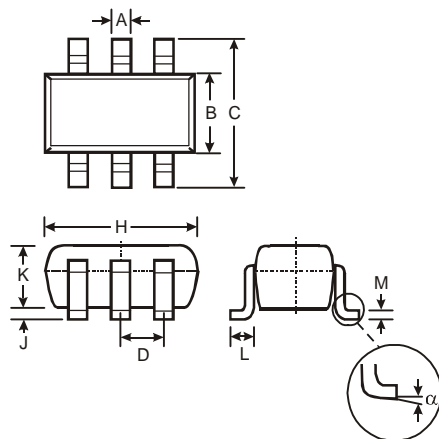
KP1 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: V = 2008)
 M = Month (ex: 9 = September)

Date Code Key

Year	2007	2008	2009	2010	2011	2012	2013	2014	2015
Code	U	V	W	X	Y	Z	A	B	C

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

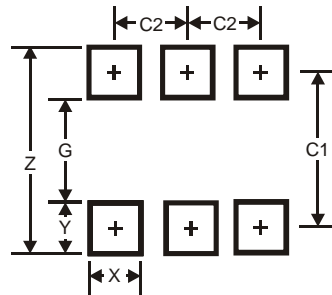
Package Outline Dimensions



SOT-26			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	—	—	0.95
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
α	0°	8°	—

All Dimensions in mm

Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
X	0.55
Y	0.80
C1	2.40
C2	0.95

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDING TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2009, Diodes Incorporated

www.diodes.com